

PATENT NUMBER

<p>O.I.P.E.</p> <p>SCANNED <i>GumD</i> <i>TM</i> Q.A.</p>	<p>PATENT DATE</p>
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Yasushi Kubota
Hajime Washio

Shift register circuit capable of reducing consumption of power with reduced capacitive load of clock signal line and image display device including it

[illegible]Form **PTO-436A**
(Rev. 6/89)

(FACE)